

SRINITHI

✉ csrinithi2004@gmail.com

☎ 8778989360

📍 109/76,Mukkayi
Thottam,Ariyagoundampatti,Na
magiripettai,Rasipuram,
Namakkal(dt) -637406.

📅 2004-01-07

♀ Female

🌐 linkedin.com/in/srinithi-c-
ba121b324

OBJECTIVE

Ready to embark on a career in development and a forward-thinking company that encourages learning and growth. Committed to using my skills to contribute positively and achieve shared goals.

LANGUAGES KNOWN

English, Tamil
Proficient

ACADEMIC QUALIFICATION

**Bachelor Degree(B.E)-Electronics
and Communication Engineering.**

- **COURSE:** B.E – Electronics and Communication Engineering
- **INSITUATION:** Muthayammal Engineering College
- **UNIVERSITY:** Anna University
- **PERCENTAGE:** 84 (2021- 2025)

HSC

- **INSITUATION:** Sri Vidhya Mandir Matriculation.Hr.Sec.School
- **BOARD:** State Board
- **PERCENTAGE:** 79 (2019 - 21)

SSLC

- **INSITUATION:** Sri Vidhya Mandir Matriculation.Hr.Sec.School
- **BOARD:** State Board
- **PERCENTAGE:** 79 (2018 - 19)

TECHNICAL SKILLS

PROGRAMMING LANGUAGES

- C, C++
- Java Programming
- HTML, CSS, Java Script

SOFTWARES KNOWN

VS Code, Eclipse, MY SQL, MS Word, Turbo c+

PROJECT

A HYBRID YOLO FPGA ARCHITECTURE FOR REAL - TIME, OBJECT DETECTION IN EDGE COMPUTING

SOFTWARE USED: Xilinx Software

DESCRIPTION: This Project aims to develop a system that leverages the speed and parallelism of Field Programmable Gate Arrays (FPGAs) to efficiently run the You Only Look Once (YOLO) object detection algorithm on edge devices, enabling fast, accurate object detection with minimal latency, ideal for applications where immediate data processing is crucial like industrial monitoring or autonomous vehicle.

CERTIFICATIONS

- Got course completion certificate for **“Programming Languages”** from CGS INFOTECH – Rasipuram.
- Fundamentals Of Artificial Intelligence – NPTEL, 2024.
- Introduction To Internet Of Things - NPTEL, 2024.
- Successfully Completed Value Added Course On **“Battery Management In E-Vehicle”** from Technology learning centre.

INTERNSHIP

**TITLE: VLSI- VERY LARGE INTREGRATED CIRCUIT IN Ed-
Vedha(1month)**

SOFTWARE USED: Xilinx Vivado,VHDL,Python.

FABRICATION PROCESS: CMOS Technology,ASIC , FPGA.

OBJECTIVE: The Internship in VLSI implementing the designing of combinational and sequential circuits by using VHDL and developing semiconductor circuits and systems. VLSI technology is essential in modern electronics, enabling the integration of millions to billions of transistors onto a chip This internship enhanced various aspects of VLSI design, including circuit simulation, logic synthesis, and layout verification.

PAPER PRESENTATION

“Microwave Technology”-AVS Engineering College, March 2024, Salem.

WORKSHOP

National Workshop on **“CST Microwave Studio & HFSS”** - Muthayammal Engineering College - (July 2023).

Workshop On **“PCB”** - M. Kumarasamy College Of Engineering, Feb 2023, Karur.

PERSONAL SKILLS

- Adaptability
- Self Motivated
- Time Management